

UNITED STATES PATENT AND TRADEMARK OFFICE Attorney Docket No. 13464US04

In the Application of:

ESIN TERZIOGLU et al.

Serial No.: 10/612,479

Filed: July 2, 2003

For: MEMORY ARCHITECTURE WITH

SINGLE PORT CELL AND DUAL

PORT (READ AND WRITE)

FUNCTIONALITY

Group Art Unit:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 2, 2003.

By: Konald E. Larson

Ronald E. Larson Reg. No. 24,478

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT AFTER APPLICATION FILING DATE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, it is respectfully requested that the following references be considered in the examination of the above-identified patent application:

Patent No.	Name	Date
5,170,375	Mattausch et al.	12/1992
5,752,264	Blake et al.	05/1998
5,781,498	Suh	07/1998
5,864,497	Suh	01/1999
5,995,422	Im et al.	11/1999
6,026,036	Sekiya et al.	02/2000
6,040,999	Hotta et al.	03/2000
6,084,807	Choi	07/2000
6,141,286	Vo et al.	10/2000

6,141,287	Mattausch	10/2000
6,144,604	Haller et al.	11/2000
6,154,413	Longwell et al.	11/2000
6,163,495	Ford et al.	12/2000
6,166,942	Vo et al.	12/2000
6,166,986	Kim	12/2000
6,166,989	Hamamoto et al.	12/2000
6,169,701	Eto et al.	01/2001
6,173,379	Poplingher et al.	01/2001

KIYOO ITOH et al., Trends in Low-Power RAM Circuit Technologies, Proceedings of the IEEE, Vol. 83, No. 4, pages 524-543, April 1995

The above-identified references are listed on the attached PTO Form 1449, and it is believed that copies of all references listed have been previously provided in the parent case, Serial No. 10/173,709, filed June 18, 2002.

This submission is in no way intended as an admission that the above-cited references constitute prior art under any subsection of 35 U.S.C. § 102. Applicant expressly retains the right to take any actions necessary to remove the above-cited references from the available prior art.

No fee is believed to be due because this statement and enclosures are being filed three months or less after the application was filed, and before the first Office Action on the merits has been mailed by the PTO. However, the Commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to Account No. 13-0017.

Respectfully submitted,

October 2, 2003

Date

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1449A/PTC Rev. 10/95	•						Complete if Known						
LIST OF PRIOR ART CITED BY							Application Number			10/612,479			
APPLICANT							Fil	Filing Date July 2, 2003					
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Sheet 1			of 1					Attorney Docket Number		13464US04			
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Examiner Cite Number Initials* No.1			Kind Code ² (if known)			Name of Patentee or Applicant of cited Document			Date of Publication of Cited Document MM-DD-YYYY				
	1	5,170,375					sch et al.			12/1992			
	2	5,752,264 5,781,498					Blake et al.			05/1998			
	3					Suh				07/1998			
	4	5,864,497				Suh			01/1999				
	5	5,995,422				Im et al.				11/1999			
	6	6,0	26,036		Sekiya e					02/2000 03/2000			
	7	6,0	40,999										
	8	6,084,807				Choi			07/2000				
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	15		66,986										
	16	6,166,989					Hamamoto et al.			12/2000			
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	the IEEE, Vol. 83, No. 4, pages 524-543, April 1995												
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1449A/PTO U.S. Department of Commerce Rev. 10/95 Patent and Trademark Office Complete if Known 10/612,479 Application Number LIST OF PRIOR ART CITED BY July 2, 2003 Filing Date **APPLICANT** First Named Inventor Esin Terzioglu et al. (use as many sheets as necessary) Group Art Unit **Examiner Name** Sheet 1 of 1 Attorney Docket Number 13464US04 U.S. Patent Document Date of Publication of Cited Kind Name of Patentee or Applicant of Examiner Cite Code²(if Number cited Document Document MM-DD-YYYY No.1 Initials* known) 5,170,375 12/1992 1 Mattausch et al. 2 5,752,264 Blake et al. 05/1998 3 5,781,498 Suh 07/1998 4 5,864,497 Suh 01/1999 5 5,995,422 Im et al. 11/1999 6 6,026,036 Sekiya et al. 02/2000 7 6,040,999 Hotta et al. 03/2000 8 6,084,807 Choi 07/2000 9 6,141,286 Vo et al. 10/2000 10 6,141,287 Mattausch 10/2000 6,144,604 Haller et al. 11/2000 11 6,154,413 12 Longwell et al. 11/2000 13 6,163,495 Ford et al. 12/2000 6,166,942 14 Vo et al. 12/2000 15 6,166,986 Kim 12/2000 16 6,166,989 Hamamoto et al. 12/2000 17 6,169,701 Eto et al. 01/2001 18 6,173,379 Poplingher et al. 01/2001 FOREIGN PATENT DOCUMENTS Foreign Patent Document Date of Name of Patentee Publication of Examiner Kind Code5 or Applicant of Cited Document Initials* Cite No.1 Office³ Number⁴ (if known) cited Document MM-DD-YYYY T^6 1 OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title Examiner Cite of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue Initials* No. 1 number(s), publisher, country, where published, source. Α KIYOO ITOH et al., Trends in Low-Power RAM Circuit Technologies, Proceedings of the IEEE, Vol. 83, No. 4, pages 524-543, April 1995 Examiner Date Signature Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation not in conformance and not considered. Include copy of this form with next communication to applicant.